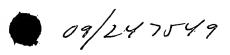
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ABSTRACT OF THE DISCLOSURE

A multiple-format video encoder stores values of the sine and cosine functions in a ROM. According to the externally specified video format, an address calculating circuit calculates the addresses at which to access the ROM. In the multiple-format video encoder, a luminance/color-difference signal generating circuit generates color-difference signals B-Y and R-Y from digital RGB signals. A first multiplying circuit multiplies the color-difference signal B-Y by the values of the sine function stored in the ROM at the addresses specified by the address calculating circuit and multiplies the color-difference signal R-Y by the values of the cosine function stored at the same addresses.